

Patent Abstracts of Japan

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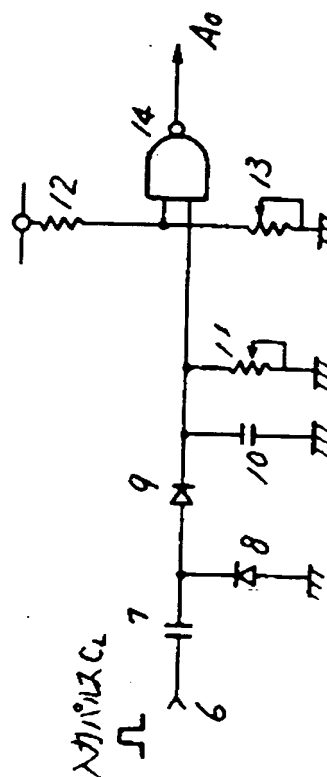
APPLICATION DATE : 16-06-81
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APPLICANT : ORIENT WATCH CO LTD;

INVENTOR : YANO KAZUYUKI;

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TITLE : INPUT PULSE DETECTING CIRCUIT



ABSTRACT : PURPOSE: To simplify the entire circuit constitution, by obtaining a function converting an input pulse into an analog signal with an integration circuit consisting of a capacitor and a resistor and a function of comparison and detection with a comparator formed with a C-MOS integrated circuit.

CONSTITUTION: A checked pulse CL inputted to an input terminal 6 passes through a DC signal blocking capacitor 7 and is rectified at diodes 8 and 9. This rectified signal is integrated at an integration circuit consisting of a capacitor 10 and a variable resistor 11 and converted into a DC voltage signal proportional to the duty cycle of the checked pulse CL. A DC voltage signal outputted from a pulse-analog signal converting section consisting of the capacitor 10 and the variable resistor 11 is inputted to a comparator 14 consisting of a C-MOS integrated circuit. Thus, the entire circuit constitution can be simplified and the integration of the entire circuit can be made easy.

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